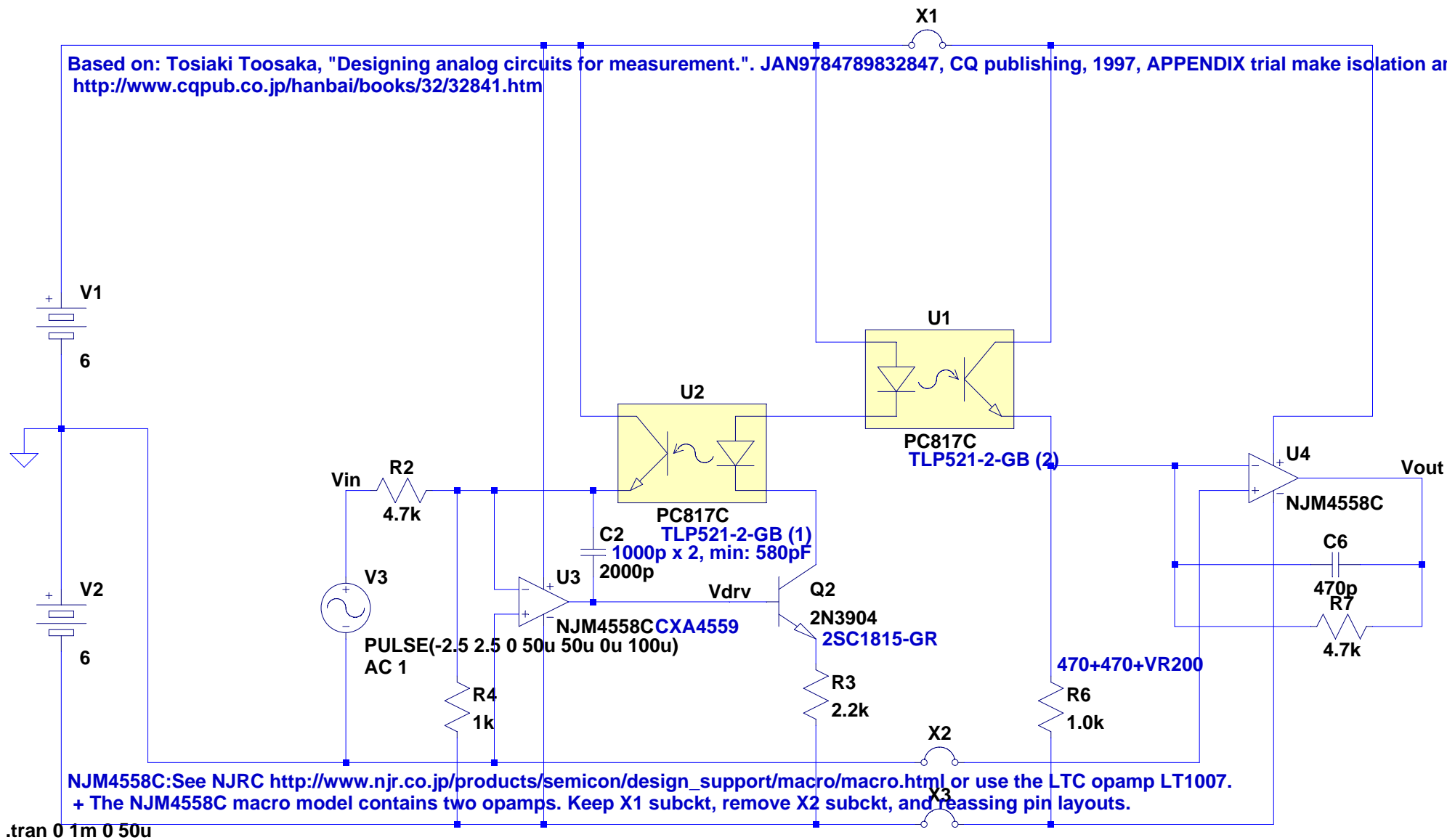


Based on: Tosiaki Toosaka, "Designing analog circuits for measurement.". JAN9784789832847, CQ publishing, 1997, APPENDIX trial make isolation amp  
<http://www.cqpub.co.jp/hanbai/books/32/32841.htm>



NJM4558C: See NJRC [http://www.njr.co.jp/products/semicon/design\\_support/macro/macro.html](http://www.njr.co.jp/products/semicon/design_support/macro/macro.html) or use the LTC opamp LT1007.  
 + The NJM4558C macro model contains two opamps. Keep X1 subckt, remove X2 subckt, and reassign pin layouts.