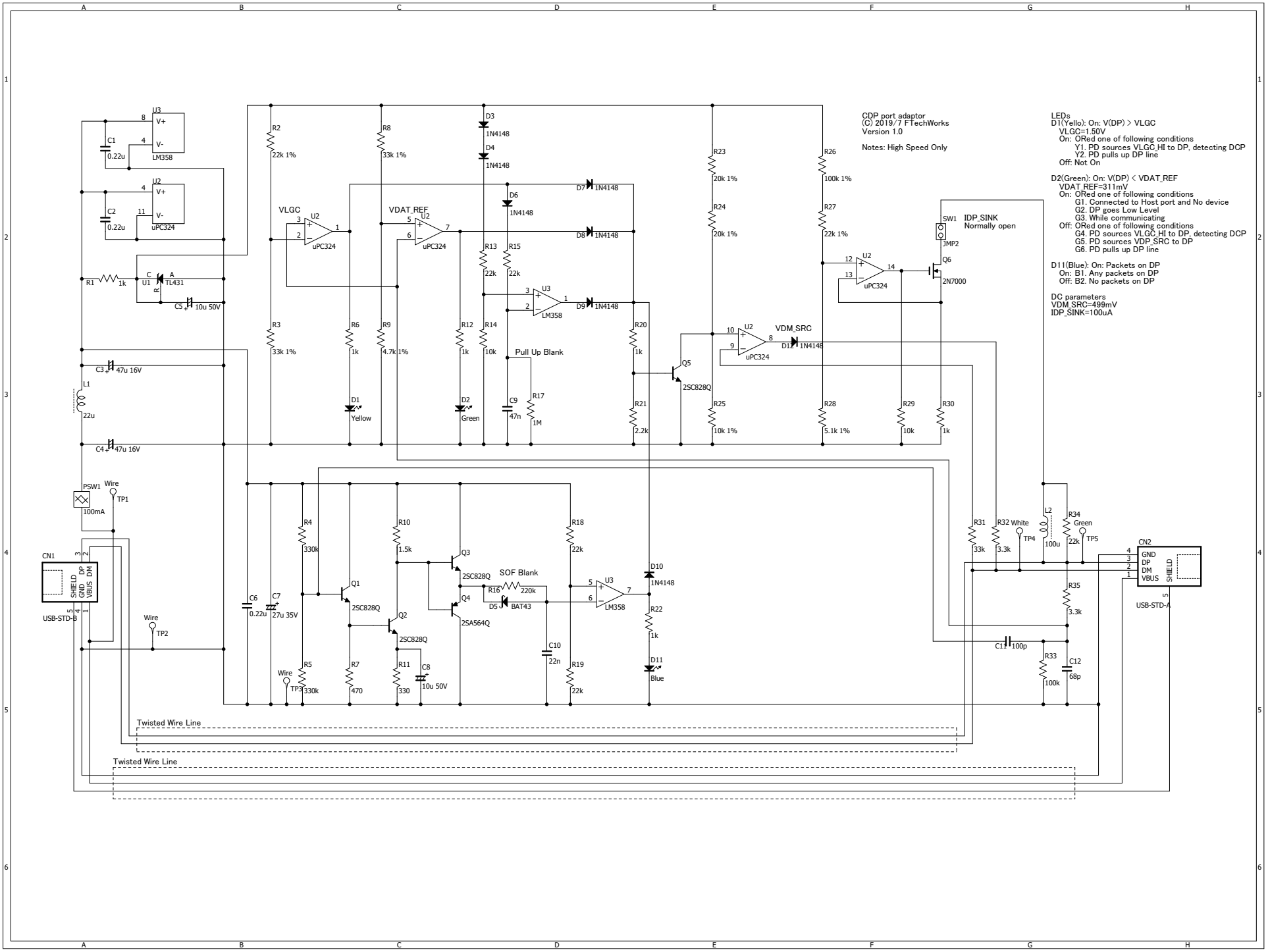
CDP port adaptor
(C) 2019/7 FTechWorks
Version 1.0
Notes: High Speed Only
LEDs
D1(Yello): On: V(DP) > VLGC
VLGC=1.50V
On: ORed one of following conditions
Y1. PD sources VLGC_HI to DP, detecting DCP
Y2. PD pulls up DP line
Off: Not On
D2(Green): On: V(DP) < VDAT_REF
VDAT_REF=311mV
On: ORed one of following conditions
G1. Connected to Host port and No device
G2. DP goes Low Level
G3. While communicating
Off: ORed one of following conditions
G4. PD sources VLGC_HI to DP, detecting DCP
G5. PD sources VDP_SRC to DP
G6. PD pulls up DP line
D11(Blue): On: Packets on DP
On: B1. Any packets on DP
Off: B2. No packets on DP
DC parameters
VDM_SRC=499mV
IDP_SINK=100uA
IDP_SINK
Normally open
VLGC
VDAT_REF
VDM_SRC
Pull Up Blank
SOF Blank
Twisted Wire Line
Twisted Wire Line
CN1
USB-STD-B
CN2
USB-STD-A
LM358
uPC324
TL431
2N7000
2SC828Q
2SA564Q
BAT43
1N4148
Yellow
Green
Blue
White
100mA